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Practitioner's Docket No. MI22-2500

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: David L. Dickerson et al.

Application No.: 10/799,794
Filed: 03/11/04

Group No.: 2812
Examiner: Unassigned

For: Isolation Region Forming Methods

Commissioner for Patents
Washington, D.C. 20231

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/799,794
Filing Date March 11, 2004
Inventor David L. Dickerson et al.
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Unassigned
Attorney's Docket No. MI22-2500
Title: Isolation Region Forming Methods

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. Copies of the cited art are included with the exception of U. S. patents and published U.S. applications (1276 Off. Gaz. Pat. Off., 05 August 2003). No admission is made regarding whether all the submitted references are prior art.

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Citation of these references is respectfully requested.

Respectfully submitted,

Date: 11-16-04



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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M22-2500	SERIAL NO. 10799,794
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT David L. Dickerson et al.	
				FILING DATE March 11, 2004	GROUP 2812

U.S. PATENT DOCUMENTS							
*Examiner Initial	Class	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
	AA	6,100,160	08/00	Honda			
	AB	6,010,947	01/00	Kondo			
	AC						
	AD						
	AE						
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FOREIGN PATENT DOCUMENTS								
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							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)			
	AR		Wojk, "Silicon Processing for the VLSI Era, Vol. 2: Process Integration," ©1990 Lattice Press, pages 51-56 (6 total pages).
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